

Light Triggering Thyristor for HVDC and other Applications.

C.K.Dwivedi.

Electrical and Electronics Engineering
Disha Inst. of mgt. And Tech. (DIMAT)
RAIPUR (C.G.) INDIA
Email-ckant.dwivedi@gmail.com

M.B.Diagavane.

Principal
S. D. College of Engineering
SEWAGRAM (MAHARASHTRA), INDIA.
Email-mdai@rediffmail.com

Abstract - High-power thyristors with direct light –triggering and integrated protection functions can be utilized advantageously for applications in which several thyristors are connected in series. This applies in particular to High-Voltage DC (HVDC) transmission, Static Var Compensation (SVC), converters for medium voltage drives, and also to certain pulse power applications. Recent progress in both device innovations and optimization of some application systems is reviewed in this article.

Key words - HVDC, Light Triggering thyristors, SVC VLF and OWT.

I.INTRODUCTION

Ever since the beginning of the thyristors era in HVDC applications in the 1960s, the blocking and current conducting capability of these power semiconductors has been improved. While in the early years about 84,000 devices rated at 0.9kA/1.65kV would have been necessary to build a 3,000 MW system, today the same amount of power can be transmitted using only 3,750 thyristors rated at 4kA/8kV. This remarkable reduction of the number of thyristors and their associated components has reduced costs for converters and increased their reliability. In parallel to this development, at the beginning of the 1980s, direct-light-triggering of thyristors was introduced. However, implementation of the direct light-trigger function into the high-voltage thyristors, and its efficient utilization for HVDC applications, became possible only recently [1-3] when laser diodes became available and stable enough (with 30-year longevity) to be used in HVDC applications. Further progress was achieved by the integration of functions for protection against over voltage pulses, voltage pulses with too high dV/dt , and voltage pulses appearing during the forward recovery time. Meanwhile it has been demonstrated that by using a new design of the Amplifying Gate (AG) structure [4, 5], all three of these protection functions can be integrated successfully into the thyristors. The basic pre-condition for this development was the continuous improvement of the quality of the silicon starting material (lower contamination level and advanced homogeneity of the receptivity distribution). Furthermore, the development of a very clean process line was required.

In this article, we first recapitulate the chosen design concept for the integration of direct light triggering and the protection functions. Then, we describe the details of an HVDC thyristors valve, and the advantages arising from the

use of direct Light-Triggered Thyristors (LTTs) with integrated protection functions. Finally, we consider applications of direct LTTs in other high-voltage AC and DC applications.

II.THYRISTORS DETAILS

A. State of art of modern Thyristor technology

In 1960 the development of thyristors (also called SCRs = silicon controlled rectifier) was started; since that time any development steps followed in order to increase the power capability of the devices and to improve the reliability. Power thyristors are manufactured from highly pure monocrystalline silicon. They are so called NPNP semiconductors. This means that they consist of four layers which are doped alternately with P and N (Fig. 1). The outer, highly doped zones are the emitting zones; the weakly doped, inner layers are the base zones. The control connection G is located on the P base; J1-J3 designates the junctions between individual zones. The off-state voltage in the reverse direction is blocked at junction J1 between P-emitter and N-base. The off-state voltage in the forward direction is blocked at junction J2 between P-base and N-base.

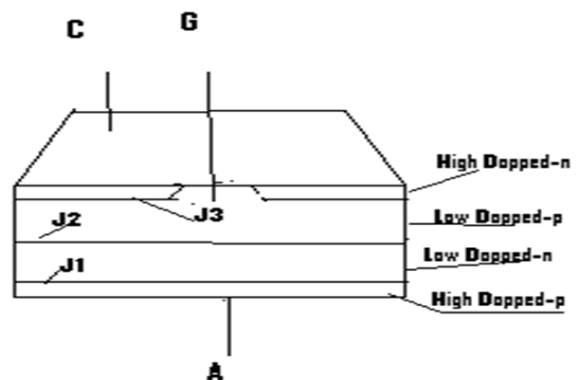


Fig. 1.Schematic cross section illustration of a high power Thyristor

Thyristors are fast but not ideal switches. Several of the imperfections of the thyristors in comparison with the ideal switch can be recognized in the static V/I-characteristic of the thyristors. In the presence of off-state voltage, an off-state current (several mA) flows both in the forward direction and in the reverse direction. Also a non-ideal static behavior of the thyristor is the on state voltage during conduction. The entire voltage drop of an HVDC thyristor is of the order of two to three volts.

This means that for typical currents several kA, considerable power losses must be dissipated. Thyristors in press pack housings are ideal for both, efficient cooling of the device and stacking for series connection. Next to the static non-ideal behavior, thyristors have also dynamic restrictions: Limited di/dt -capability after turning on, as well as the reverse recovery behavior including turn-off time has to be considered in the design of the powers stack.



Fig. 2 High power thyristors made of 4", 5" and 6" silicon wafer

There is a trend towards higher transmission current capability of long-distance HVDC systems. With this trend, the requirements for higher current capabilities arise. On the other hand, the blocking voltage of about 8 kV per thyristor was derived as an optimum of overall operational losses. As a consequence, a 6 inch thyristor with a blocking voltage of 8 kV (repetitive blocking voltage) was developed. This thyristor is capable to be utilized for dc transmission with currents up to 4500 A. Due to the joining of the silicon wafer with a molybdenum carrier disc, the required surge current capability could be reached with an excellent high safety margin. These immense current capabilities make the thyristor also interesting for other applications with high current requirements and high blocking voltage needs.

B. Direct Light-triggering & Over voltage Protection

The central areas of the thyristors consist of a Break-Over Diode (BOD) and a four-stage AG structure (Fig. 3). The BOD is located inside the light-sensitive area. If the forward biased thyristors is illuminated by a short light pulse (typical duration = 10 ns, light power = 40 mW, $\lambda = 940$ nm), the generated electron-hole pairs are immediately separated in the space charge region of the p-n-junction formed by the central parts of the p-base and the n-base. The hole current generated in this way provides the trigger current for the AG structure. If an over voltage pulse in forward direction is applied to the thyristors, the BOD provides a trigger current to turn the thyristors on. This is achieved by the curved shape of the p-n junction which ensures that the electric field always has a maximum value at the BOD when a forward voltage is applied to the thyristors. Thus, when the device voltage exceeds a certain threshold, avalanche generation starts in the BOD, providing a trigger current to turn on the thyristors safely by means of the AG structure. The blocking voltage of the BOD can be adjusted by the curvature of the central p-n junction and the distance between the BOD and the p-ring below the optical gate. Details are described in Refs. [1] and [3]. However, an easy post-

processing adjustment of the breakdown voltage can be done by irradiating a certain area around the BOD with helium ions or protons. To this end, the particle energy is chosen such that the penetration depth is larger than the depth of the p-n junction. While helium irradiation generates acceptor-like defects, proton irradiation followed by an annealing step at temperatures between 220°C and 300°C causes the appearance of hydrogen-related donors. In the first case, the breakdown voltage can be increased by several hundred volts; and in the second case, a well-defined decrease of the breakdown voltage is possible [6].

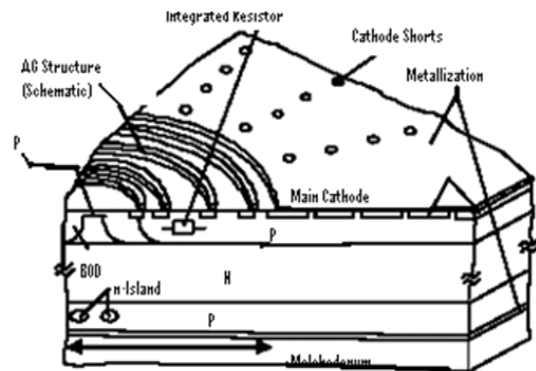


Fig. 3: Central part of the thyristors (BOD & four stage AG structure) and part of the main cathode area (from Ref. [4]).

C. dV/dt Protection

Integration of a dV/dt protection function can be achieved by designing the AG structure and the main cathode area such, that the innermost AG has higher dV/dt sensitivity than the other AGs and the main cathode area. This ensures that the thyristors are turned on safely by the AG structure when a voltage pulse with a dV/dt ramp exceeding the dV/dt capability of the innermost AG is applied to the thyristors. However, care has to be taken to fulfill the requirements concerning the light sensitivity and the dV/dt capability of the thyristors. Both features are mainly determined by the doping concentration of the p-base and the depth, width, and radial position of the n+-emitter, i.e. by the design of the innermost AG.

D. Forward Recovery Protection

Commutation of a thyristors leads to an extraction of the free charge carriers in the n-base and the p-base and the formation of an anode-side depletion layer in the n-base. Since the depletion layer extends only to a certain depth into the n-base, excess carriers remain in its quasi-neutral plasma region, recombining there at a certain rate. When large regions of the thyristors have completely recovered and an uncontrolled forward voltage pulse appears, the remaining small region with excess carriers in the main cathode area may turn on. This may result in current filamentation, self-heating and, finally, in local destruction of the thyristors. To avoid this, the thyristors should also be turned on in a controlled way by the AG structure if such a failure occurs. An integration of a corresponding protection function into the AG structure is not so easy, because the AGs usually

turn off earlier than the main cathode area does. Consequently, the carrier concentration in the AG region is typically lower compared with the main cathode area. However, an increase of the carrier concentration in the AG region can be achieved by modifying the carrier's lifetime distribution so that it is lower in the main cathode area ($r > r_c$) than in the AG structure [4,5,7-9]. This can be realized with relatively little effort by using a masked electron irradiation instead of the homogeneous one that is usually applied to adjust the on-state voltage.

Re-triggering of the thyristors by means of the AG structure in the case of forward voltage pulses during the forward recovery phase can be further supported by phosphorus islands located inside the p+-emitter of the inner AGs (Fig. 3). The resulting anode-side n-p-n structures operate as local transistors when a reverse voltage is applied to the device. Their carrier injection can be controlled by the doping concentration and the dimensions of the islands. Thyristors designed in this way (i.e. provided with both non-homogeneous carrier lifetime distribution and local phosphorus islands) were subjected to extensive test measurements monitoring their response behavior on voltage pulses appearing during the forward recovery period. These test measurements demonstrated the effectiveness of the integrated forward recovery protection function [4, 5].

E. Direct Light-triggering vs. Electrical-triggering

Because of the high-voltage environment of thyristors in HVDC applications rated up to 500 kV it is necessary to electrically separate the trigger unit at ground potential from the thyristors at high-voltage potential. The trigger commands for the thyristors are transmitted as a light pulse via a fiber-optic cable, even if Electrically Triggered Thyristors (ETTs) are used. Therefore, the possibility of triggering the thyristors directly with the help of a light pulse was considered early. That would make it possible to avoid converting the low-power light pulse into a high-power electrical gate pulse by a printed circuit board at thyristors potential when using an ETT (Fig. 4 a & b). Thus, using LTTs instead of ETTs significantly reduces the number of electronic parts necessary to trigger and to protect a thyristors, and thus increases the converter's reliability. The amount of light needed to trigger an LTT is about 1,000 times higher than the amount of light needed to transmit the trigger command to the printed circuit board associated to an ETT. Although the principle of triggering a thyristors directly by light has been known for more than 30 years, LTT application in HVDC has been feasible only since sufficiently powerful and reliable light sources and optical components with low damping characteristics became commercially available. A special challenge was the integration of protection functions into mass-produced thyristors. Once this was accomplished, the number of electronic components could be reduced to that necessary for monitoring, and no more firing pulses would be generated at the thyristors level. This significantly increases the electromagnetic immunity of the system.

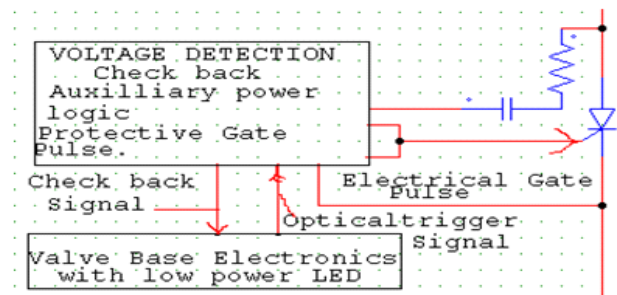


Fig 4 (a) ETT

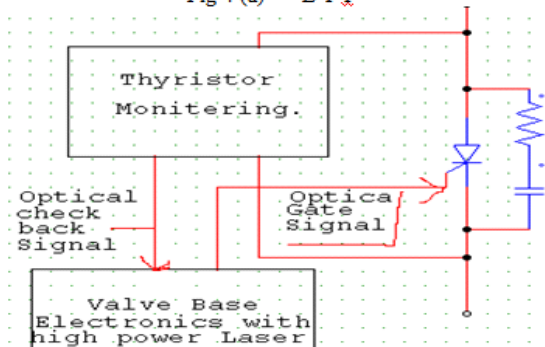


Fig. 4 (b) LTT

Fig. 4: Triggering and monitoring of thyristors, LTT versus ETT.

III. STATUS QUO OF HVDC VALVES WITH LIGHT-TRIGGERED THYRISTORS

A demonstration valve with 44 units of 4"/8kV LTTs with integrated over voltage and dV/dt protection function has been in operation at the Cello converter station of the Pacific Intertie USA since Oct. 1997 [10]. The valve has performed flawlessly for 5 years; no component failed or caused a malfunction of the valve. Based on existing contracts, by the end of 2004 about 5,500 units of 4"/8kV-LTT and 3,800 units of 5"/8kV-LTT will be in commercial operation.

A. Optical Gating System

The major components of the gating system are:

- A commercially available laser diode (LD) as a light emitting device with a lifetime of more than 30 years of operation in a HVDC application.
- The optical link between the Valve Base Electronics (VBE) and the LTT. The link consists of light guides and an optical mixer, a so-called Multimode Star Coupler (MSC).

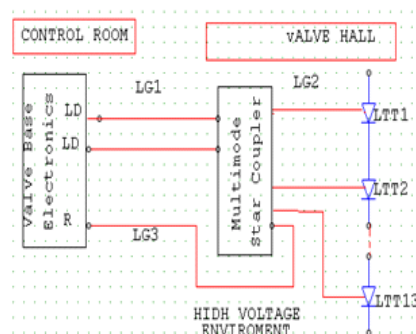


Fig. 5: Optical components for gating the LTT's of one valve section.

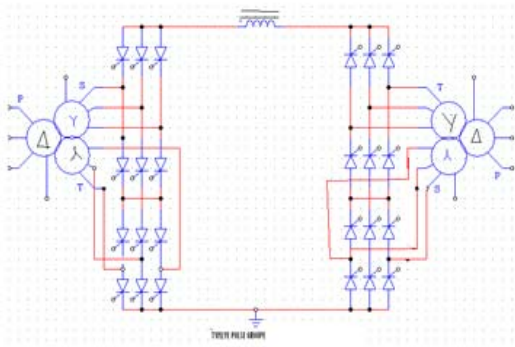


Fig. 6: Schematic diagram of a HVDC converter.

The fiber-optic cables, LG 1, connect the LD to the MSC as shown in Fig. 4. The light pulses from the three LDs are mixed and distributed equally between the thirteen outputs. Additional fiber-optic cables, LG 2, then carry the light pulse directly to the thyristor gates. The light from one output of each MSC is routed back to the VBE (via LG 3) for monitoring purposes. One LD is redundant and the three diodes are installed on different PCB's, each of which can be changed without risk during operation.

B. Mechanical Design of the Thyristor Valves

A 500-kV HVDC converter consists of twelve Thyristor valves arranged in two serially connected 3-phase-bridges forming a twelve pulse group (Fig. 5). The mechanical design is based on an arrangement of three quadruple valve (Fig. 5) towers for a twelve pulse group. Each valve tower comprises four valves; each valve is made up of three Thyristor modules. These twelve modules are arranged in a twin tower suspended from the valve hall ceiling. The high-voltage end is at the bottom and includes separate corona shields (Fig. 6). Each valve typically includes 78 serially connected thyristors with a peak blocking voltage of 8 kV. The snubber circuits (Fig. 7) are made from a single capacitor and resistor. The capacitor is filled with SF₆ rather than oil, to minimize the fire hazard. The resistor is of the "wire-in-water" type for maximum heat dissipation. A separate grading capacitor is used to linearize the voltage distribution along the valve for steep-fronted surges. It is also filled with SF₆. The thyristors are monitored using a printed circuit board (TVM) associated with each LTT.



Fig. 7: Quadruple valve tower (photograph taken in the high-voltage test field of the University of Karlsruhe / Germany).

All plastic materials for tubing and insulation have flame-retardant self-extinguishing characteristics in accordance with UL 94-V0. All heat-dissipating components (thyristors, snubber resistors and valve reactors) are cooled with de-ionized water in a strictly parallel cooling circuit, a design which has proven its resistance to corrosion in nearly 20 years of service. Mechanically, the serially-connected thyristors levels of one valve are grouped into six sections of typically 13 levels each. The thyristors and their heat sinks are mounted in a pressure contact stack with the snubber circuits, water distribution pipes, and TVM boards arranged on either side. The valve reactor, typically consisting of three serially-connected sub-units and the grading capacitor, is located at the end of the stack. All active components of two valve sections rest on a surrounding aluminum frame which serves as a mechanical support and corona shield, and constitutes a modular unit. The arrangement of the thyristors and heat sinks in the stack and their associated equipment is a straight-forward image of the electric circuit diagram. Uniform voltage grading and ease of testing are advantages of this design.

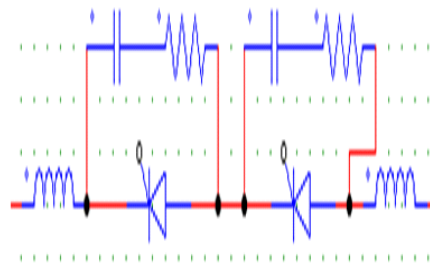


Fig. 8: Schematic diagram of a thyristor modular Unit (2 valve sections).

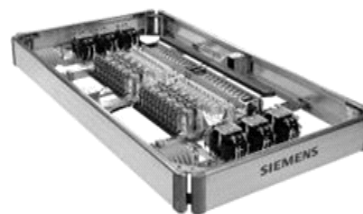


Fig. 9: Typical HVDC Thyristor module with LTT technology

IV.HIGH-VOLTAGE AC AND DC APPLICATIONS

The possibility of easy series connection favors the application of direct LTTs not only for HVDC transmission lines, but also for various other applications. The whole spectrum of eupec high voltage thyristors — all with integrated BOD and dV/dt protection function and with blocking voltages between 5.2 and 8 kV and pellet diameters from 2 up to 5 inch — can be used in the following examples.

A. Medium-voltage Drives

Medium-voltage (MV) drives are getting more and more important, because of their cost effective performance. Existing MV drive products range between 2.3 and 7.2 kV, tending to higher voltages of up to 15 kV. The main drives are mostly built with IGBT modules in inverters with two or

more levels. For high-end voltage and power systems, Press Pack IGBT's and even LTTs (frequencies 50 - 110 Hz) may be of interest because direct series connection with redundancy of more than two semiconductors can be accomplished in one level inverters. In soft starters, which work with full sine waves, commutated thyristors can be used. In the upper voltage region where series connection of semiconductors is necessary, the use of smaller LTTs (eupec T553N) is a big advantage.

B. Mobile High-voltage Cable Test Systems

High-voltage network maintenance is necessary to ensure supply reliability. Measurement and location of partial discharges is internationally accepted as an indication of insulation system integrity. Such test systems are built like a resonant circuit (Fig. 10). When the LTTs are triggered they form together with the test object and an air-cored inductor a series resonant circuit. The inductance of the air core is selected such that the resonant frequency is similar to the power frequency of the service voltage. High voltage cable insulation usually has a relatively low dissipation factor. Combined with the low loss factor of the air-cored inductor, this provides a high-Q resonant circuit. The specially developed measurement circuit detects all partial discharge pulses that occur during the oscillating wave. The partial discharge pulses are localized by the traveling wave method. Because it is easy to connect LTTs in series, the devices will be used in a mobile high-voltage cable test system for **250 kV**.

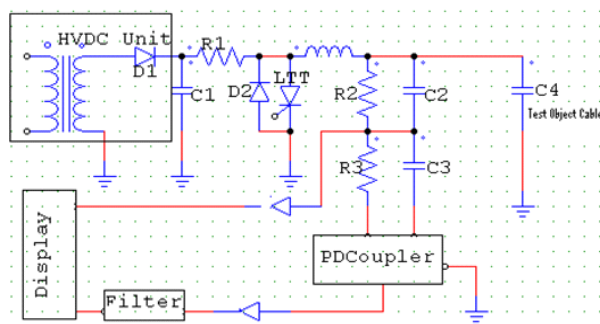


Fig. 10: Schematic for a cable test system

C. Pulse-power Applications

Because of the high dI/dt -capability, LTTs can be used in many pulse-power applications such as preservation of liquid food, fast magnetic forming, crow bars and high-power lasers. For such pulse-power applications the energy is normally stored in capacitors that are discharged in one or several pulses with high dI/dt and peak current. **Preservation of liquid food** is achieved by high-voltage arc discharges. The energy is stored in several capacitors. By triggering serially-connected LTTs, the voltage pulse is transformed to a higher level to trigger the spark gap (Fig. 10). There are three main mechanisms that cause microbial inactivation during arc discharge treatment: Shock waves, UV-radiation, and the formation of radicals. The advantages of this method are its cost effectiveness and that the taste of the food is not

modified by pasteurization. This kind of applications normally works with a frequency of several ten Hz.

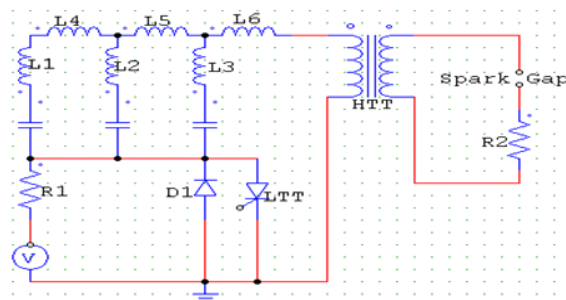


Fig. 11: Schematic for preservation of liquid food

Fast magnetic forming is becoming more and more important, especially for the automotive industry. For fast magnetic forming applications, normally a capacitor is charged to several kV, and then the LTT is triggered by a laser pulse. The capacitor is discharged into a special air coil. A crow-bar diode protects the capacitor against negative charging. The object that is supposed to be deformed is placed in a special air coil. In this way, for example, two pipes can be fixed together without using any kind of rubber seal LTTs are tested in this application with charging voltage of up to 5 kV, a load dI/dt of 5 kA/μs and a maximum load current of 35 kA. LTTs are used in **crow bar applications** to protect expensive and sensitive components such as klystrons or high-power capacitor banks. The LTTs are switched directly in parallel to the part that should be protected (Fig. 11). In case of over voltage or high dV/dt in the system, the internal protection function triggers the LTTs and the current commutates to the thyristors. In case of any other failures or irregular functions, the LTTs are triggered by the laser pulse and the expensive parts are protected. LTTs are tested in this application with a charging voltage of 65 kV and a dI/dt of about 5 kA/μs.

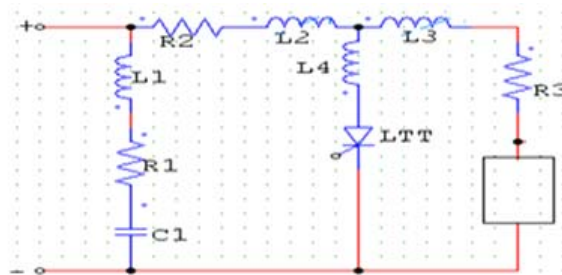


Fig. 12: Simplified schematic for a crow bar circuit

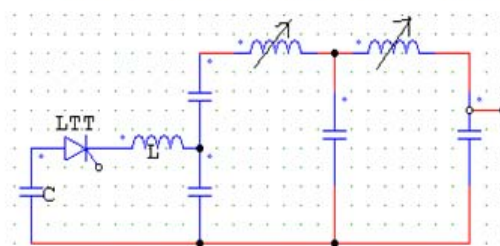


Fig. 13: Simplified schematic for laser application

High-power lasers are used in lithography processes to produce processors and TFT displays. First, a capacitor bank is charged to several kilovolts (Fig. 12). Then, the LTTs are triggered and the current flows through a coil to produce a well-defined current pulse. This current pulse is compressed by two or more Pulse-compression circuits. After this compression, the current pulse triggers the laser. These laser applications normally work at a frequency of several 100 Hz. LTTs are tested in this application with a charging voltage of 16 kV, a load dI/dt of 2 kA/μs, a maximum load current of 2 kA, and a frequency of 300 Hz. *Ignitrons* are used in several existing applications. These devices will be replaced because of the pollution that can be caused by the mercury in them. LTTs are a good solution for most of these replacements.

V. SUMMARY

In conclusion, we point out that light-triggered thyristors combined with self-protection functions can be employed with great advantage to improve the reliability of such systems as HVDC transmission lines and other high-voltage AC and DC applications.

REFERENCES

- [1] H.-J. Schulze, M. Ruff, B. Baur, F. Pfirsch, H. Kabza, U. Kellner: "Light Triggered 8 kV Thyristors with a New Type of Integrated Breakover Diode", Proc. 8th Int. Symp. on Power Semiconductor Devices and IC's, Maui Hawaii USA, pp.197-200, 1996.
- [2] M. Ruff, H.-J. Schulze, and U. Kellner, "Progress in the development of an 8-kV light-triggered thyristor with integrated protection functions", IEEE (ED), vol. 46, no. 8, pp. 1768-1774, 1999.
- [3] F.-J. Niedernostheide, H.-J. Schulze, and U. Kellner-Werdehausen, "Self-protection functions in direct light-triggered high-power thyristors", Microelectronics Journal, vol. 32, no. 5-6, pp.457-462, 2001.
- [4] F.-J. Niedernostheide, H.-J. Schulze, U. Kellner-Werdehausen, "Self-Protected High-Power Thyristors", Proc. PCIM'2001, Power Conversion, Nuremberg, Germany, pp. 51-56, 2001.
- [5] H.-J. Schulze, F.-J. Niedernostheide, U. Kellner-Werdehausen, "Thyristor with Integrated Forward Recovery Protection Function", Proc. of 2001 Int. Symp. on Power Semiconductor Devices and IC's, Osaka, Japan, pp. 199-202, 2001.
- [6] H.-J. Schulze, F.-J. Niedernostheide, M. Schmitt, U. Kellner-Werdehausen, G. Wachutka, "Influence of Irradiation-Induced Defects on the Electrical Performance of Power Devices", in: High-Purity Silicon VII, ECS-PV, in print (2003).
- [7] O.L. Harding, P.D. Taylor, and P.J. Frith, "Recent advances in high voltage thyristor design", Fourth International Conference on AC and DC Power Transmission, London, pp. 315-319, 1985.
- [8] R.W. Aliwell and D.E. Cress, "Development of a self-protected light triggered thyristor", IEE Colloquium Devices, Drives, Circuits, and Protection, London, pp. 7/1-3, 1994.
- [9] M.S. Towers and P. Mawby, "Self protected light triggered thyristors", IEE Colloquium Recent Advances in Power Devices, London, p. 8/1-10, 1999.

[10] H.P. Lips, J. Matern, R. Neubert, L. Popp, M. Uder, "Light Triggered Thyristor Valve For HVDC Application", Proc. of 7th European Conference on Power Electronics and Applications, Volume 1, pp. 1.287-1.292, Trondheim, Norway, 1997.

[11] H.-J. Schulze, F.-J. Niedernostheide, M. Schmitt, U. Kellner-Werdehausen, G. Wachutka, "Influence of Irradiation-Induced Defects on the Electrical Performance of Power Devices", in: High-Purity Silicon VII, ECS-PV 2002-20, pp. 320-335 (2002).

[12] J. Przybilla, R. Keller, U. Kellner, C. Schneider, H.-J. Schulze, F.-J. Niedernostheide "Direct light triggered solid-state switches for Pulsed- Power- Applications" 14th IEEE Pulsed Power Conference 2003, Dallas/Texas

[13] J. Przybilla, R. Keller, U. Kellner, C. Schneider, H.-J. Schulze, F.-J. Niedernostheide "Advantages in Application-Design by using Direct-Light-Triggered Thyristors" PCIM China 2004, Shanghai

[14] R. Keller, " Bipolare Silizium Hochleistungshalbleiter für Pulsed Power" Elektronik Praxis, Mai 2004, pp. 74 – 76

BIBLIOGRAPHY



Chandra Kant Dwivedi received the *B.Sc (Engg.)* Degree in Electrical Engineering from B.I.T. Sindri, Ranchi University, India in 1971. He has telescopic experience of more than 32 years in the field of Electrical testing & commissioning of electrical equipments, H.T. & L.T. switch gears at floor level in steel industries. He guided the team of electrical engineers for testing and commissioning of electrics at Bokaro, Bhilai, Rourkela and Visage steel plant. Since Feb, 2008, he is **Sr. Lecturer** in Electrical & Electronics Engineering at DIMAT, RAIPUR (C.G). His main areas of interest are High voltage engineering, Testing & commissioning of electrical installations. He is pursuing M.E. Degree by research from Nagpur University, INDIA.



Dr. Manoj B. Daigavane obtained the M.E. Degree in Power Electronics Engineering from Nagpur University, India in 1988. He received the M.S. Degree in Electronics and Control Engineering from Birla Institute of Technology and Science, Pilani (Raj) India in 1994. He also obtained the M.E. Degree in Power Electronics Engineering from Rajeev Gandhi University of Technology, Bhopal (M.P), India in 2001. He received Ph D Degree in Electrical Engineering from RSTM Nagpur University, India in 2009. Since Sept. 1988- June 2007, he had been with the Department of Electronics and Power Electronics Engineering, B. D. College of Engineering, Sewagram (Wardha), affiliated to the Nagpur University, India. Since July 1, 2007 to Apr 30, 2009, he was Professor & Head of Electrical and Electronics Engineering, Disha Institute of Mgmt. and Tech., Raipur (C.G.) where he is engaged in teaching & research. Presently, he is Principal of S. D. College of Engineering, Wardha – Maharashtra (India), since May 01, 2009. His main areas of interest are resonant converters, Power quality issues, DSP applications and Power electronics for motor drives. He has been responsible for the development of Electrical Machines and Power Electronics Laboratories. He is a Member of the Institution of Engineers (India) and a Life Member of the Indian Society for technical Education.